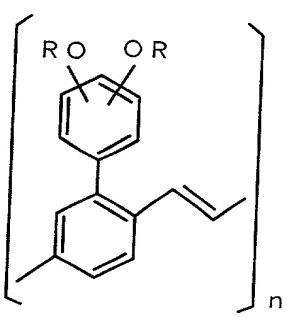
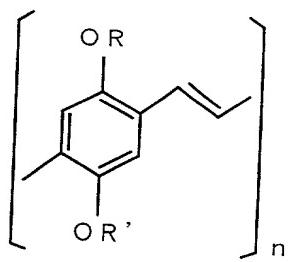


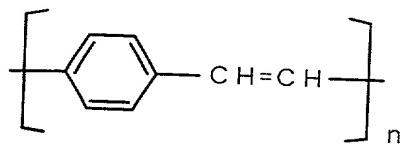
Chem. 1



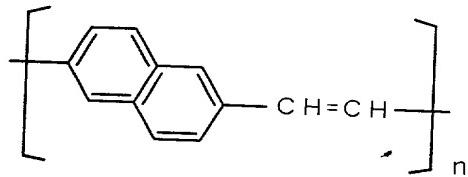
Chem. 2



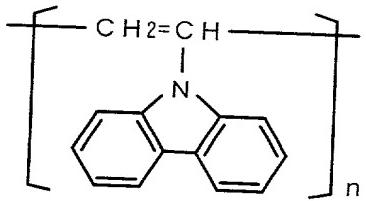
Chem. 3



Chem. 4



Chem. 5



100 200 300 400 500 600 700 800 900

Fig. 1A

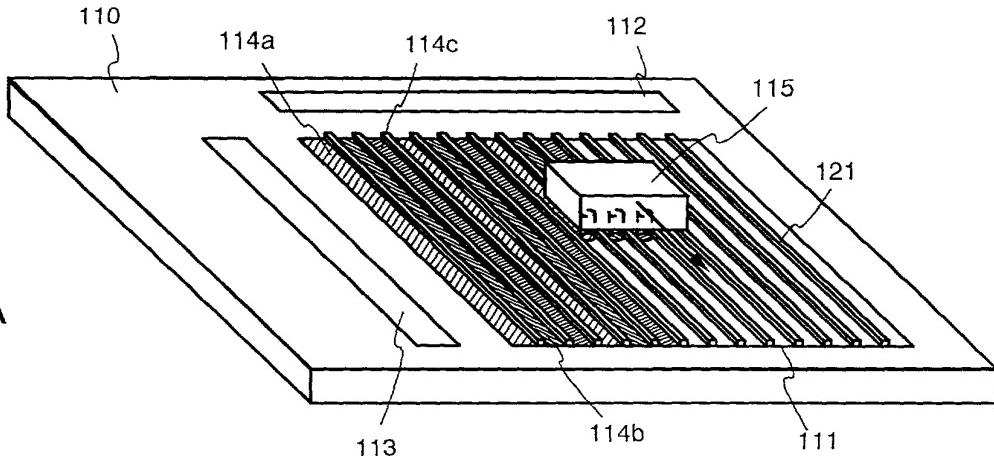


Fig. 1B

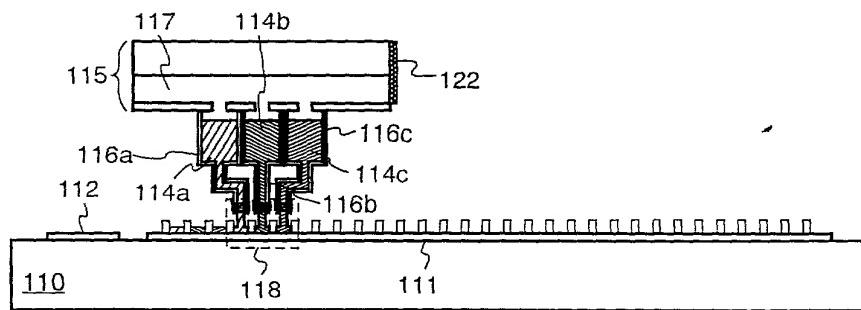
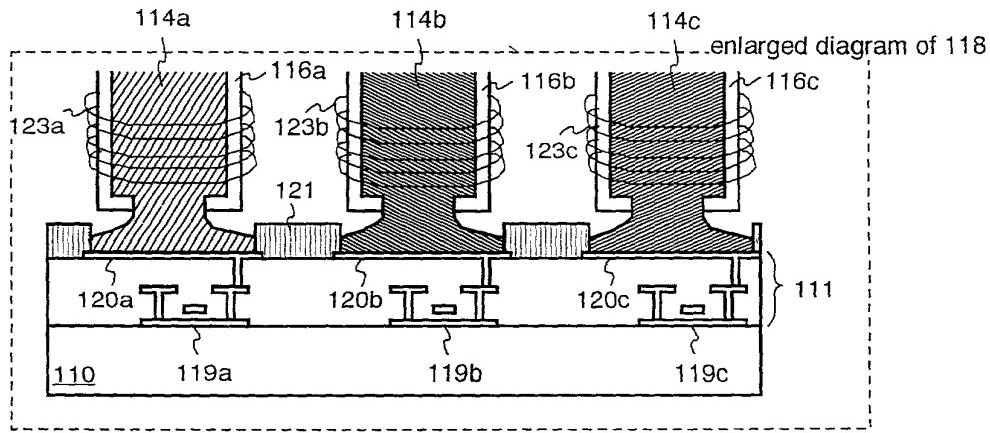
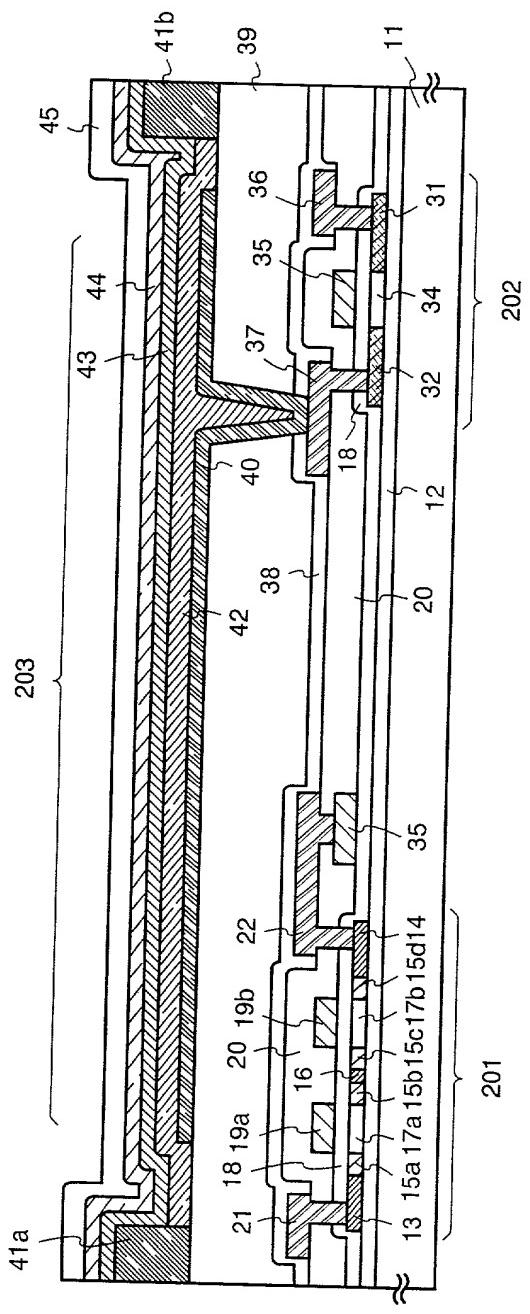


Fig. 1C





11:substrate 12:base film 13:source region 14:drain region 15a-15d:LDD region  
 16:high concentration impurity region 17a,17b:channel forming region 18:gate insulating film  
 19a,19b:gate electrode 20:first interlayer insulating film 21:source wiring 22:drain wiring  
 23:gate electrode 31:source region 32:drain region 34:channel forming region 35:gate electrode  
 36:source wiring 37:drain wiring 38:first passivation film 39:second interlayer insulating film  
 40:pixel electrode(anode) 41:bank 42:EL layer 43:cathode 44:protecting electrode  
 45:second passivation film

Fig. 2

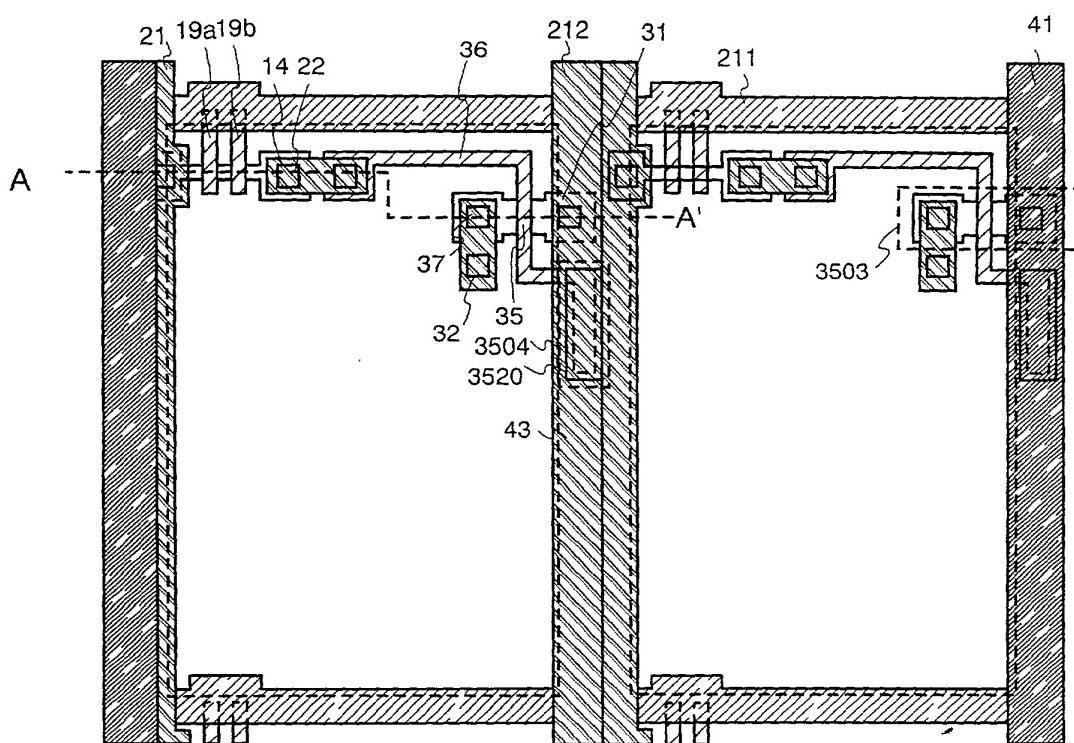


Fig. 3A

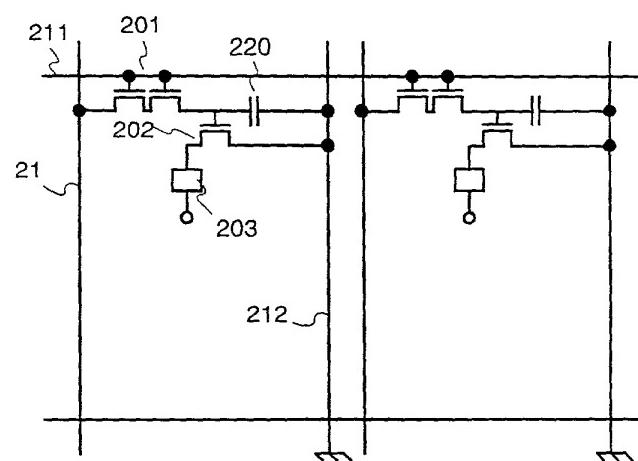
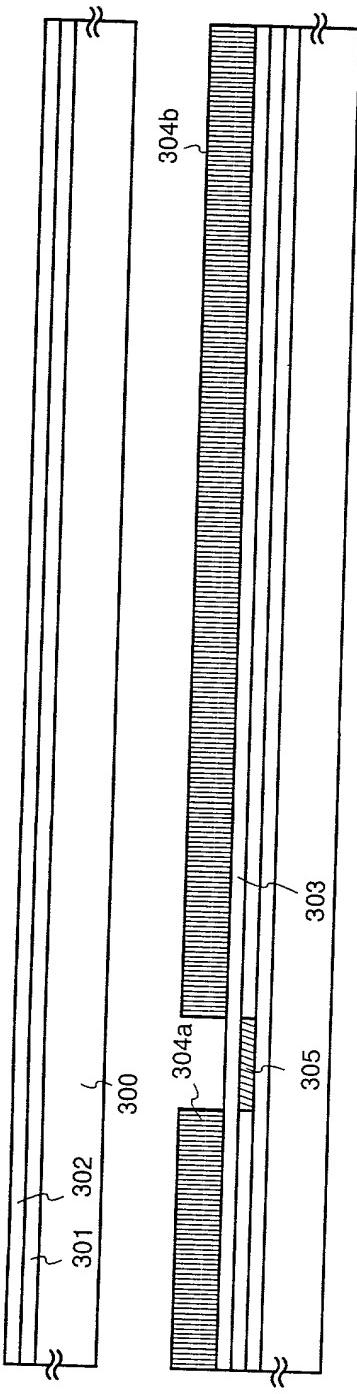


Fig. 3B

**Fig. 4A**

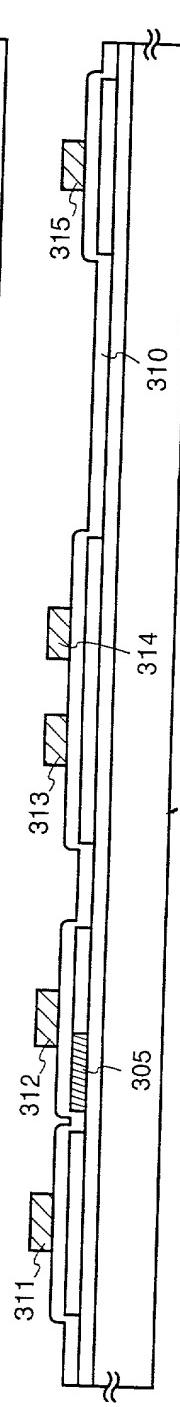


**Fig. 4B**

**Fig. 4C**



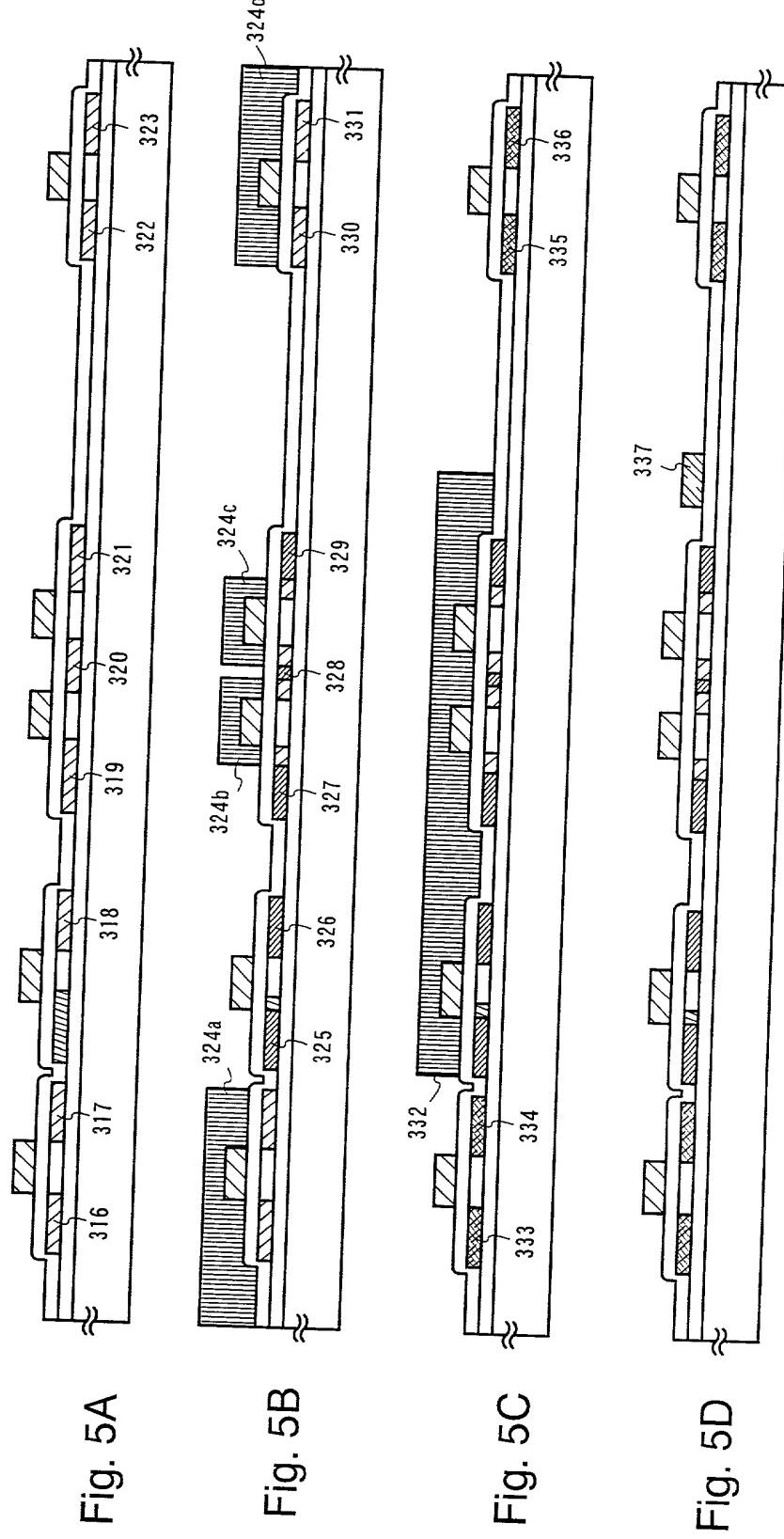
**Fig. 4D**



**Fig. 4E**

300:glass substrate 301:base film 302:polysilicon film 303:protecting film 304a,304b:resist mask  
305:n-type impurity region 306-309:active layer 310:gate insulating film 311-315:gate electrode

316-323:n-type impurity region(c)  
324a-324d, 332:Resist Mask  
333-336:p-type impurity region(a)  
325-329:n-type impurity region(a)  
337:Gate Wiring



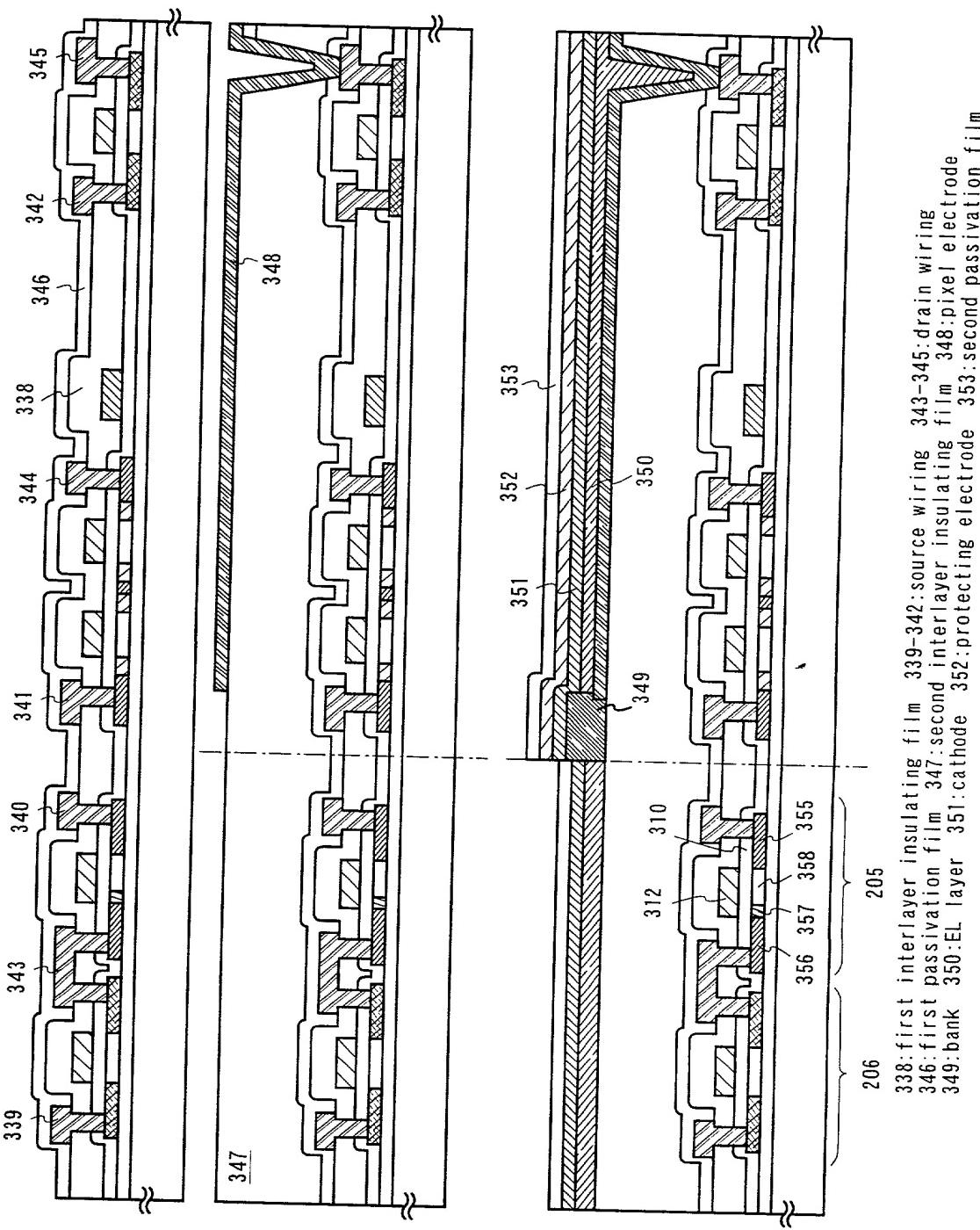


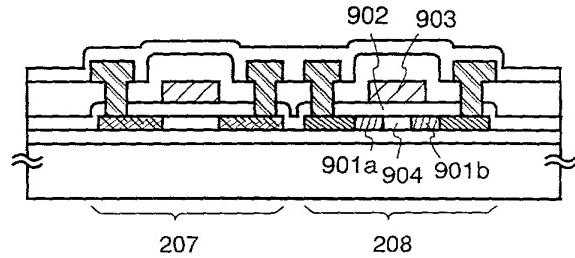
Fig. 6A

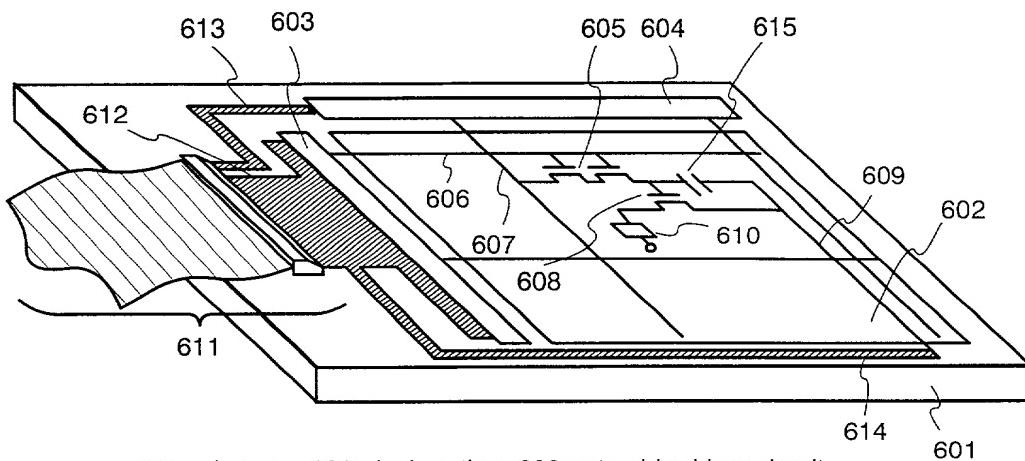
Fig. 6B

Fig. 6C

338: first interlayer insulating film  
 339-342: source wiring  
 343-345: drain wiring  
 346: second interlayer insulating film  
 347: first passivation film  
 348: pixel electrode  
 349: EL layer  
 350: cathode  
 351: protecting electrode  
 352: second passivation film  
 353: hatched area  
 355: bank  
 356, 357, 358: contacts  
 359: contact  
 360: contact  
 205: hatched area  
 206: hatched area

Fig. 7





601:substrate 602:pixel portion 603:gate side driver circuit  
604:source side driver circuit 605:switching TFT 606:gate wiring  
607:source wiring 608:current control TFT 609:electric power supply  
610:EL element 611:FPC 612-614:input/output terminal 615:capacitor

Fig. 8

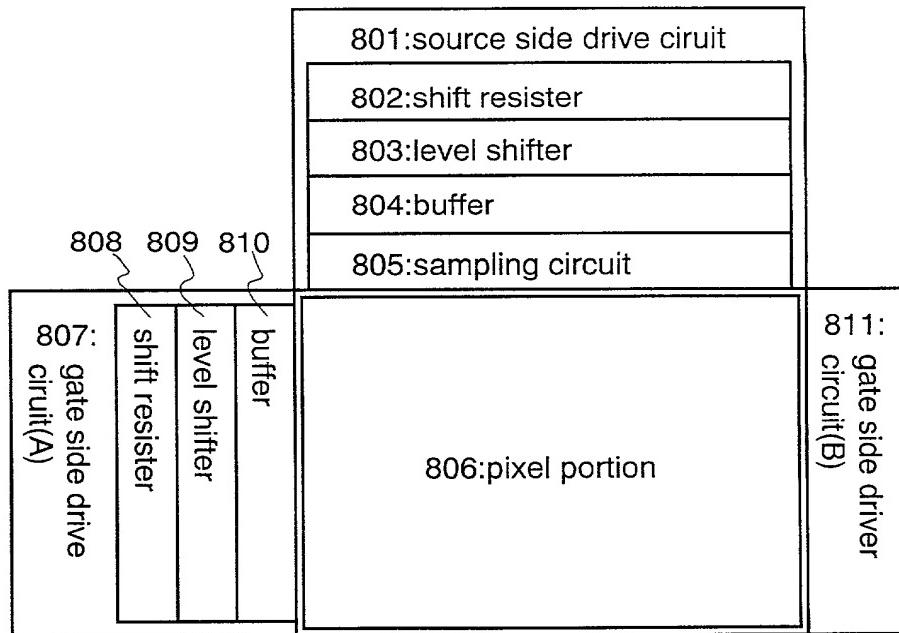


Fig. 9

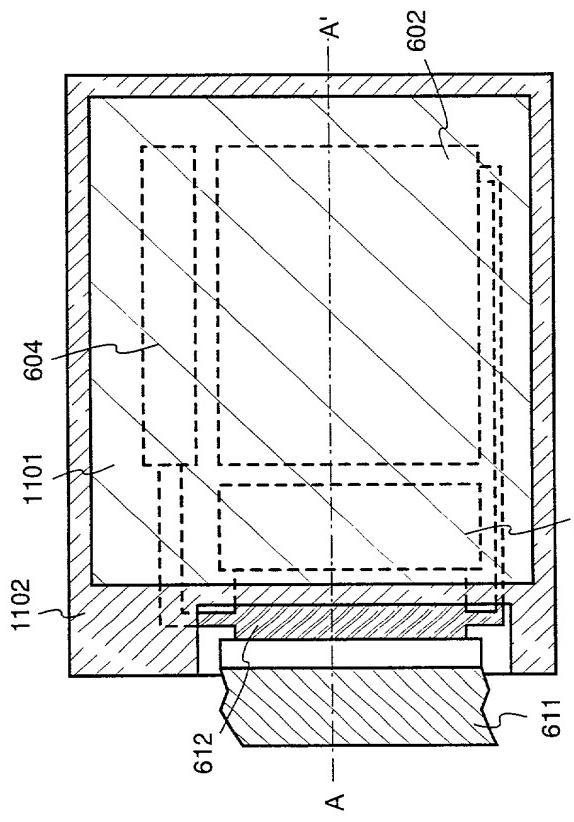


Fig. 10A

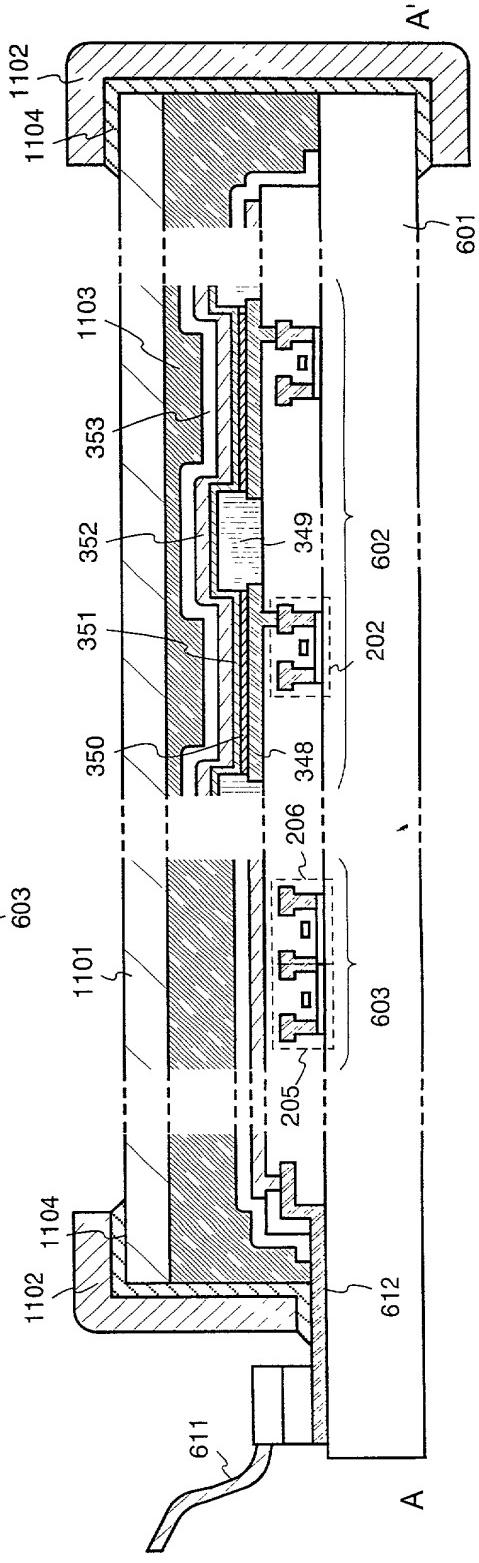


Fig. 10B

Fig. 11A

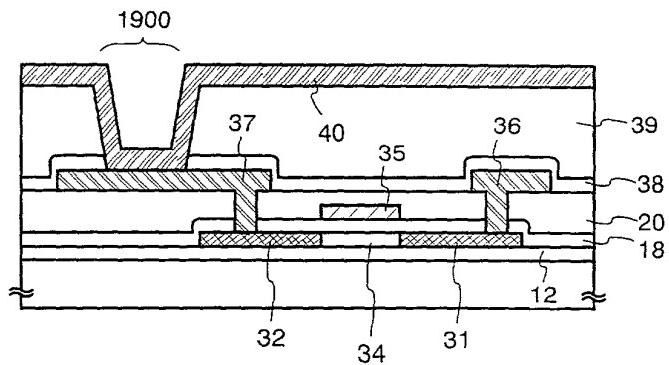


Fig. 11B

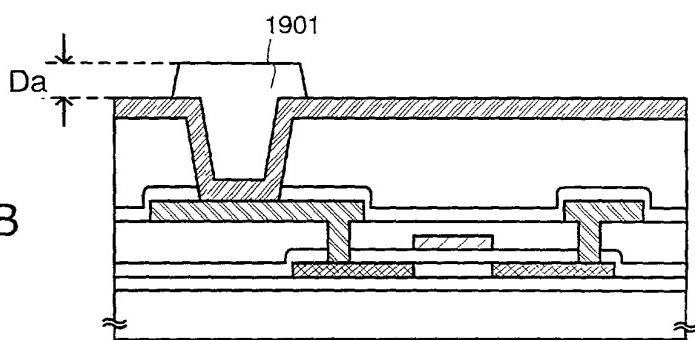
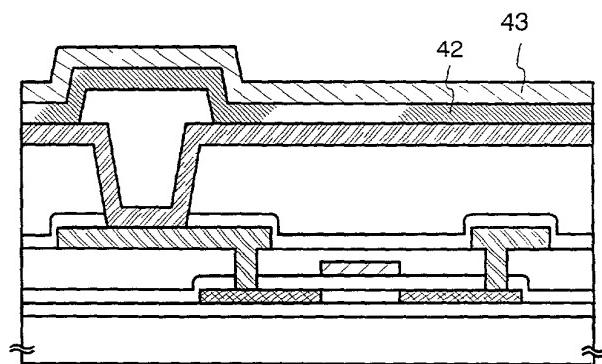


Fig. 11C



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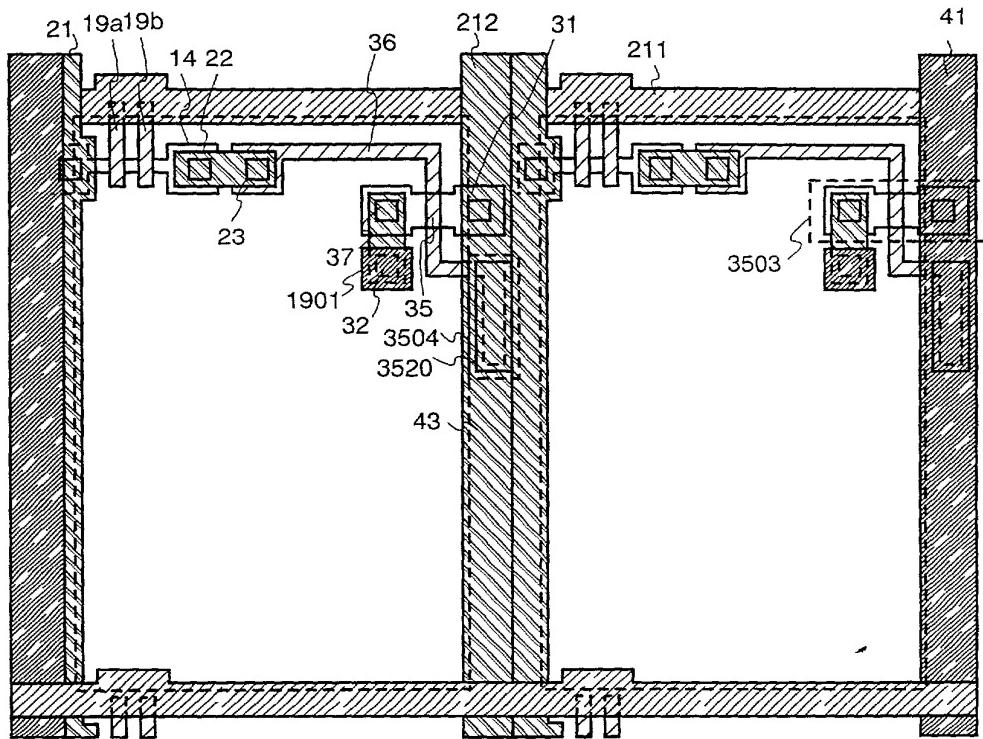


Fig. 12

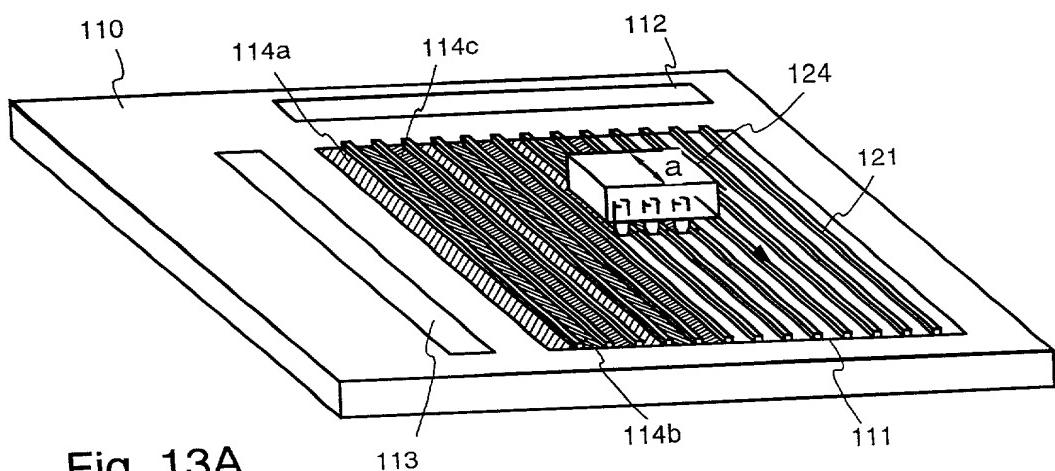


Fig. 13A

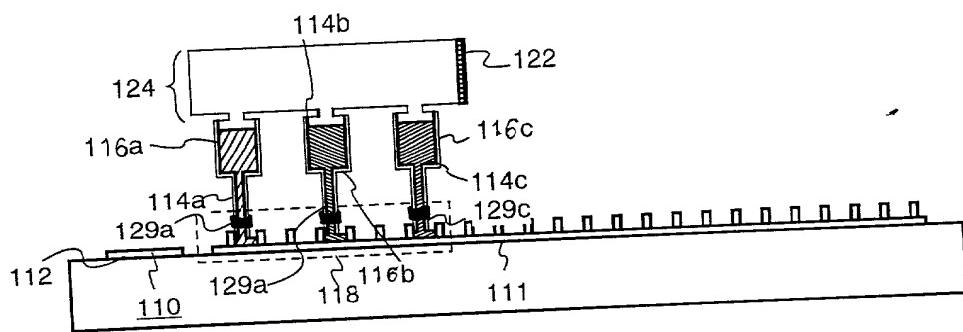


Fig. 13B

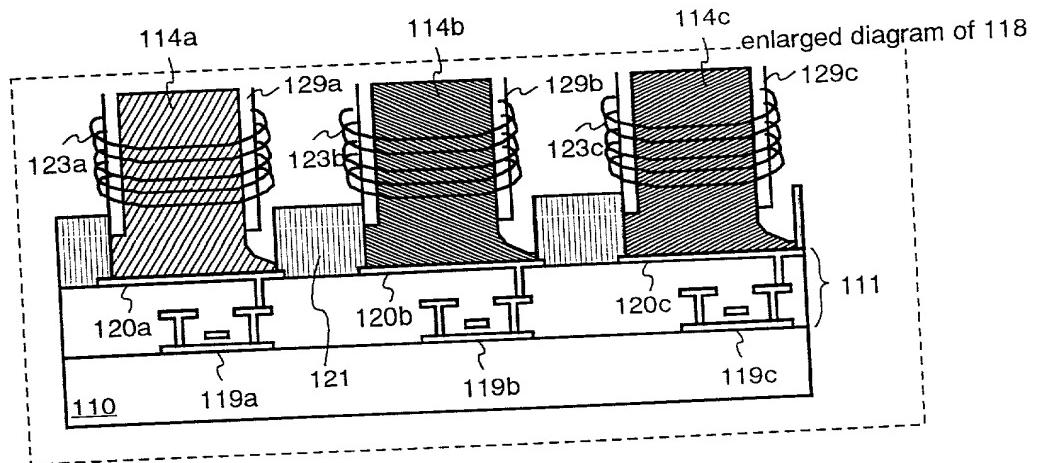


Fig. 13C

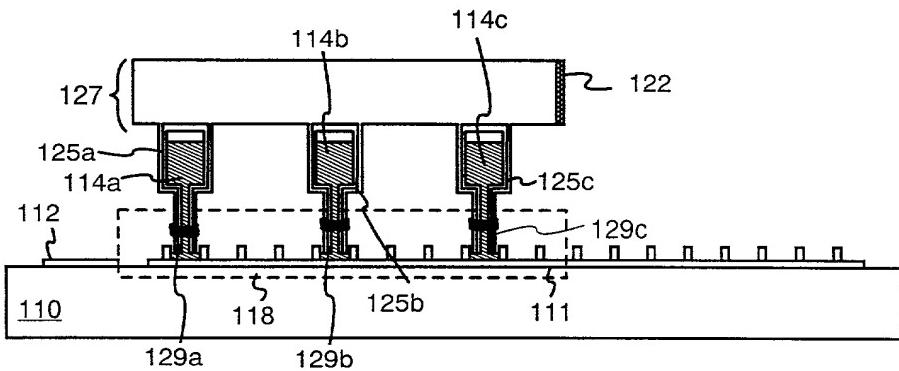


Fig. 14A

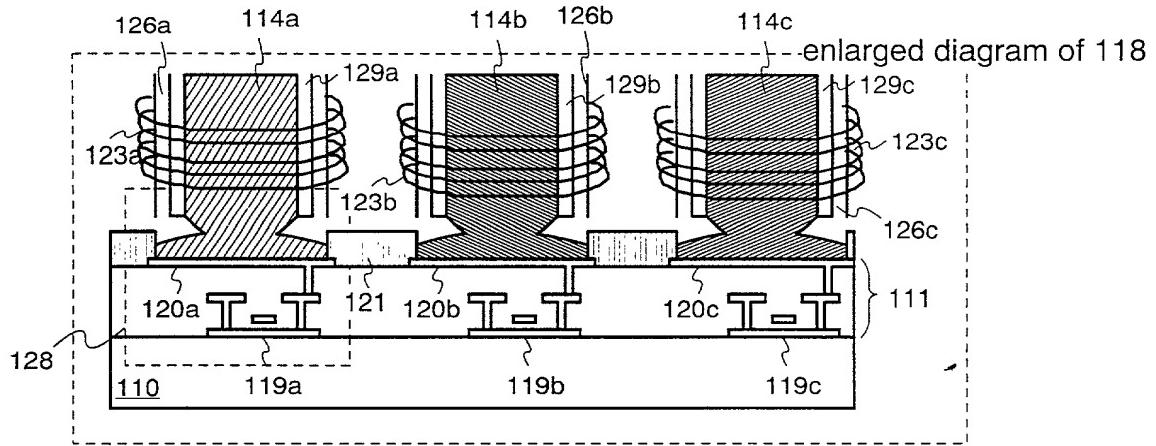


Fig. 14B

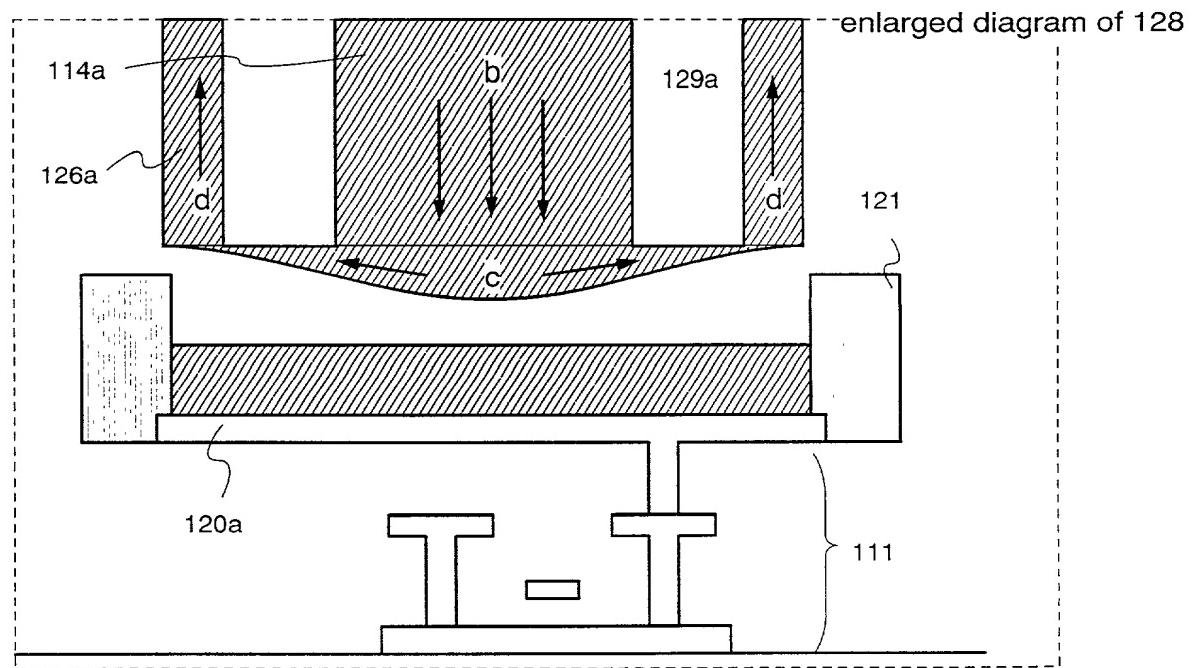
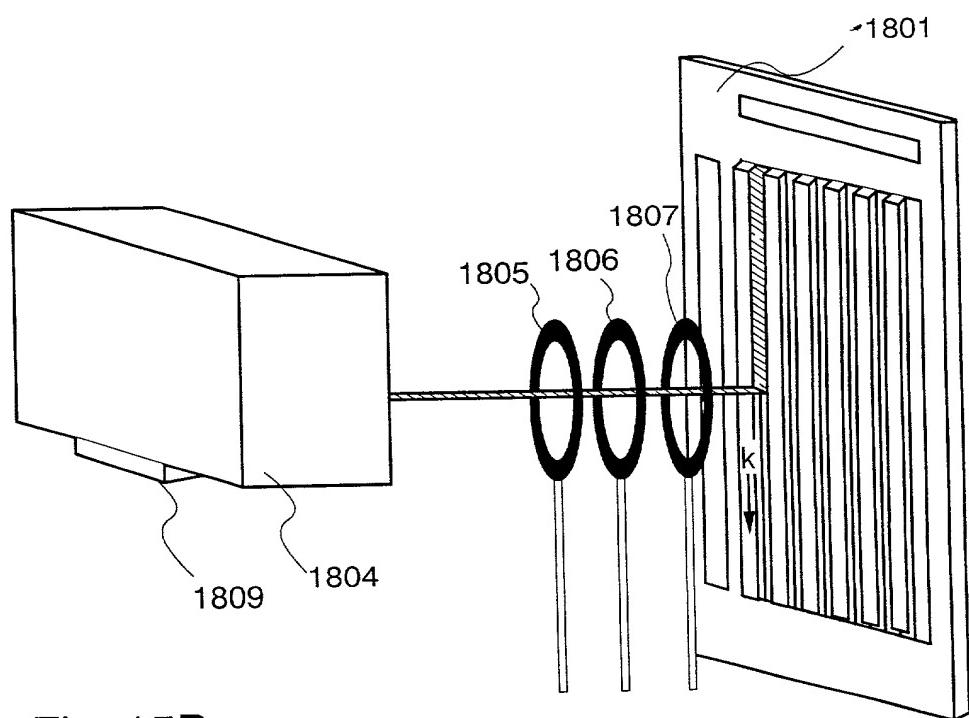
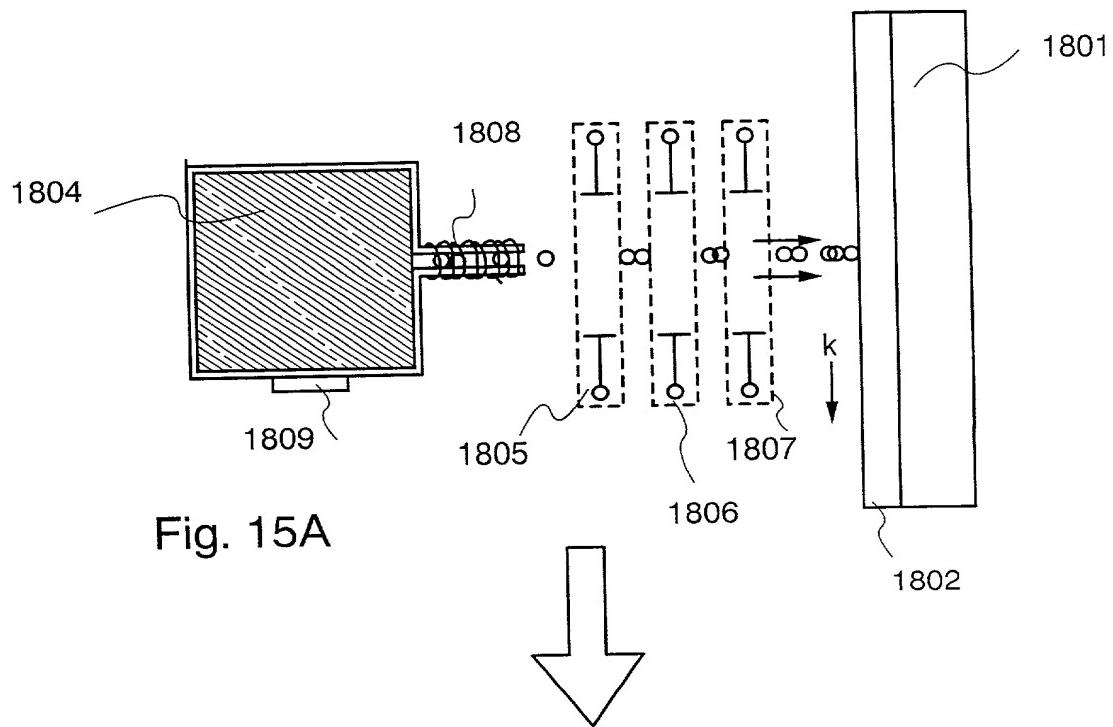
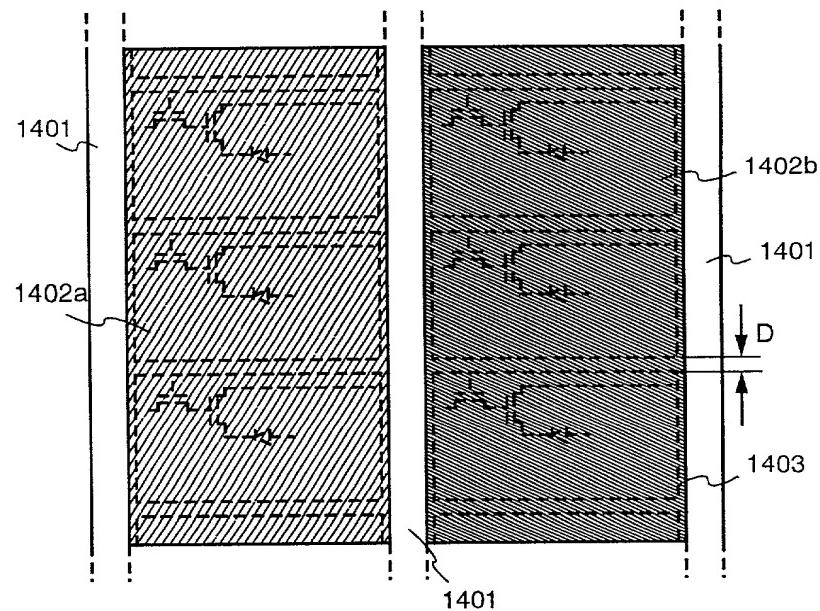


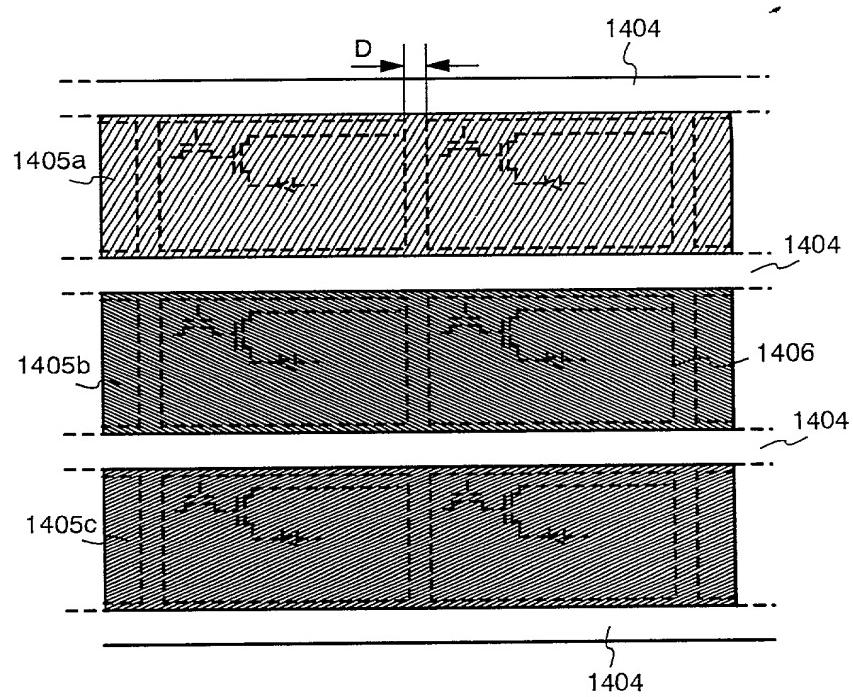
Fig. 14C



**Fig. 16A**



**Fig. 16B**



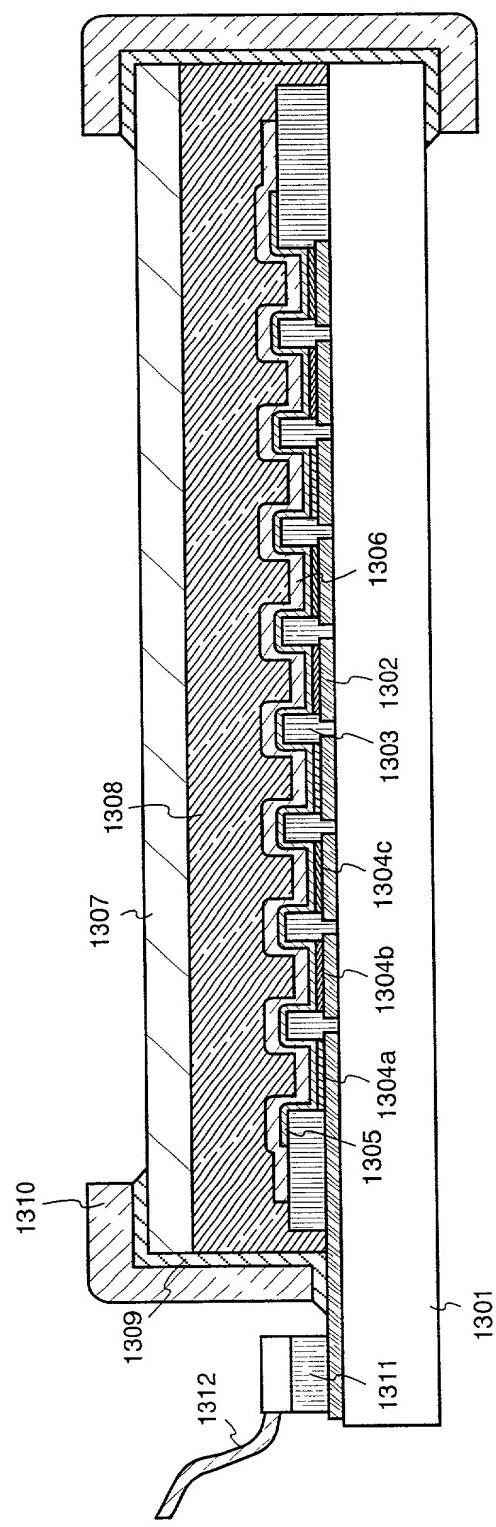


Fig. 17

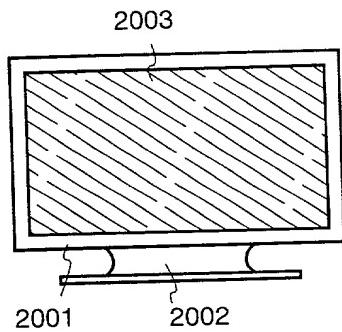


Fig. 18A

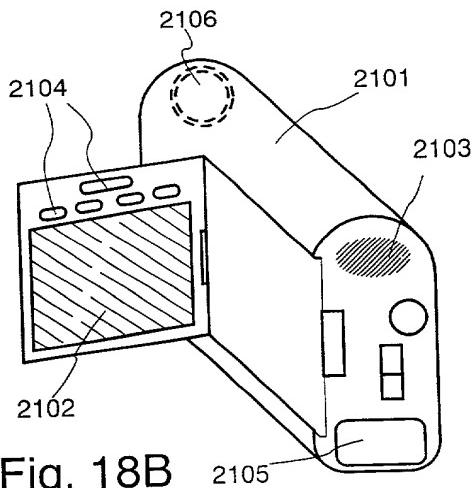


Fig. 18B

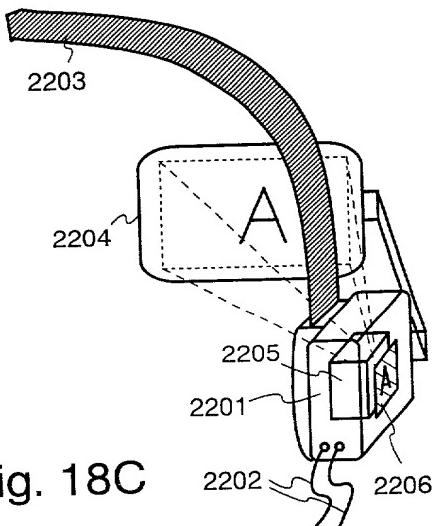


Fig. 18C

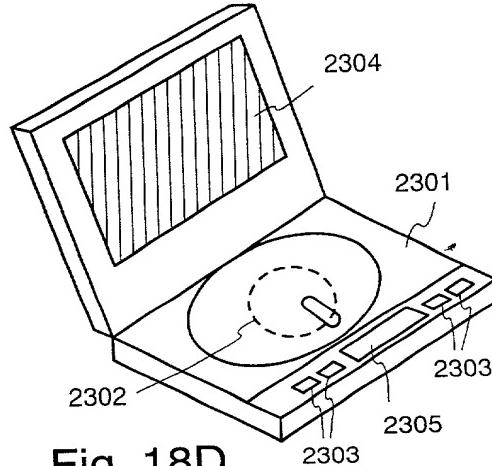


Fig. 18D

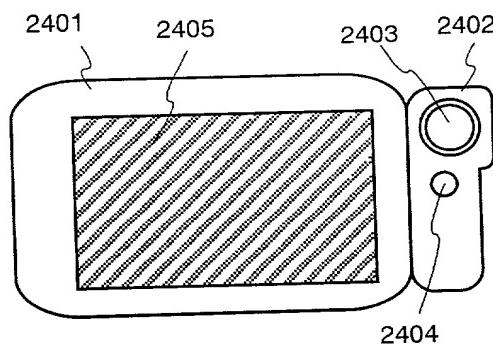


Fig. 18E

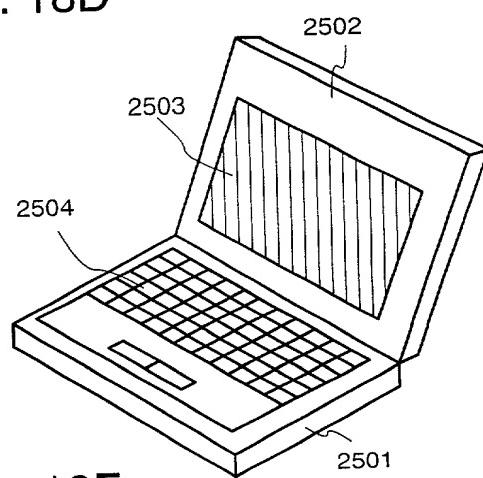


Fig. 18F

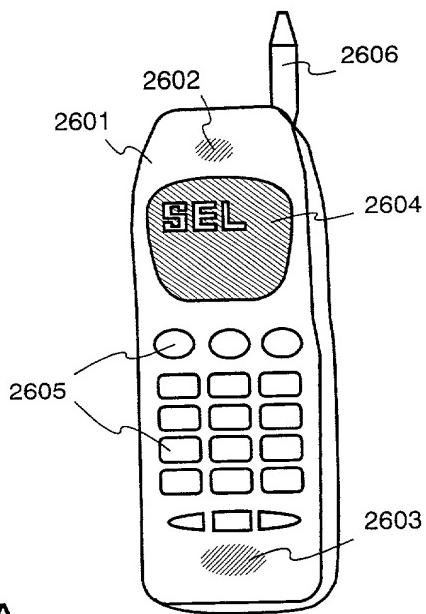


Fig. 19A

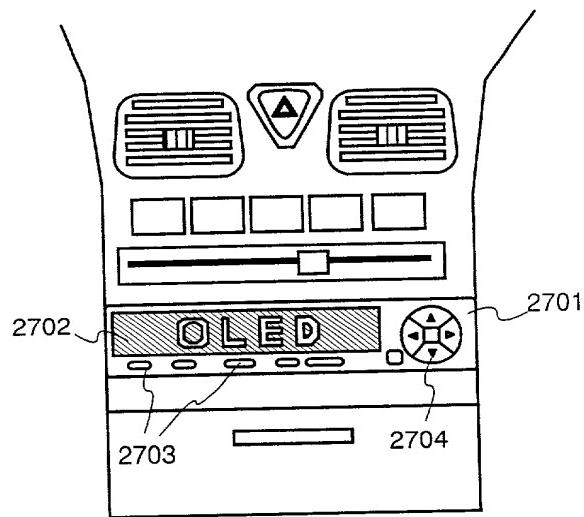


Fig. 19B

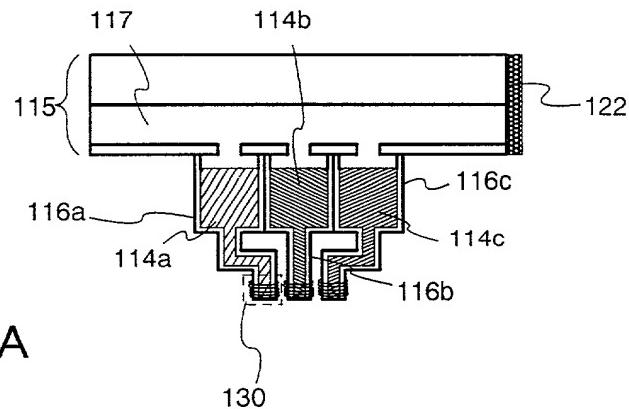


Fig. 20A

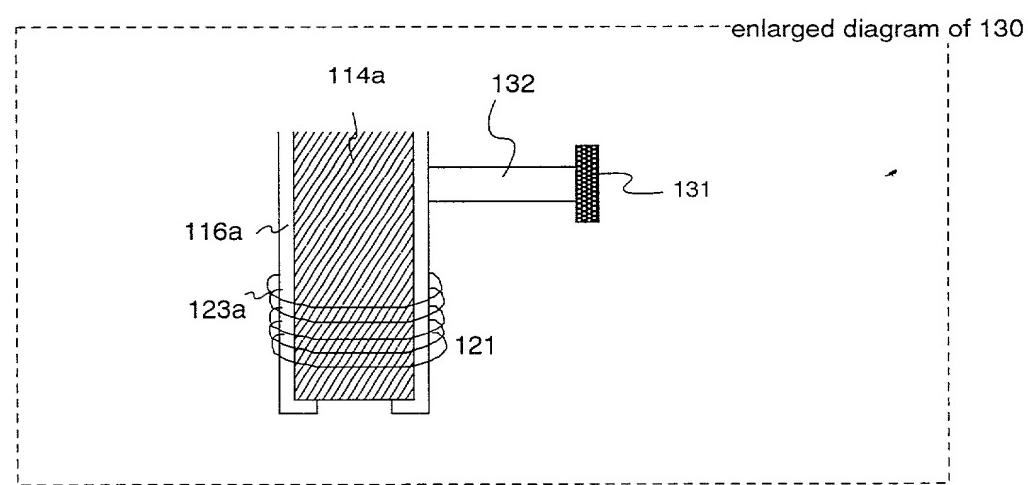


Fig. 20B